# PARSOLPS

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# PARSOLPS project

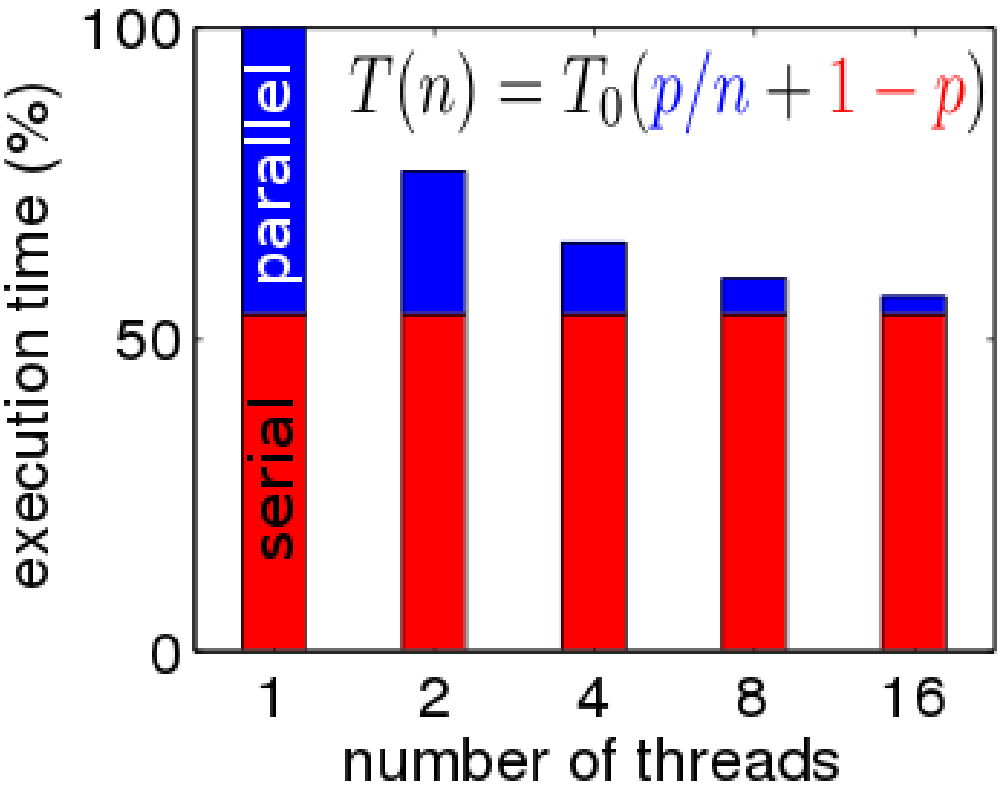

- Scrape-Off Layer Plasma Simulation
- Main components:
  - B2: fluid code for edge plasmas
  - EIRENE: Monte-Carlo code for neutral transport
- Improve parallel performance of SOLPS 5.0
  - OpenMP parallelization of B2.5
  - Couple with EIRENE

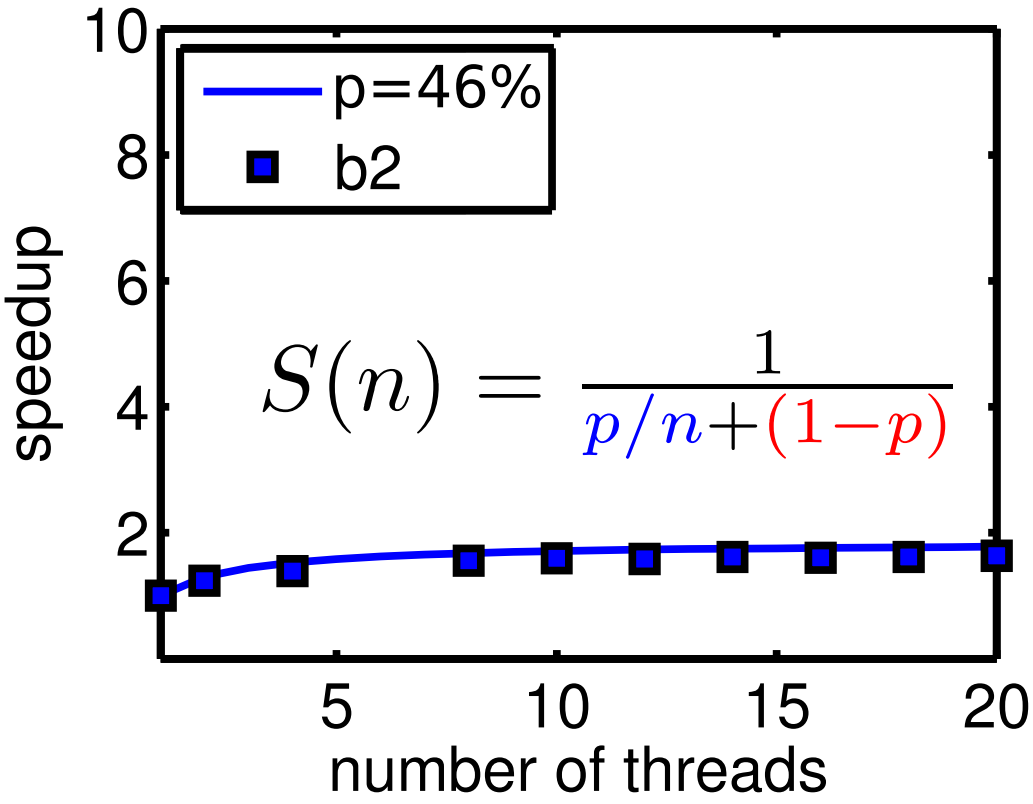
# Outline

- Quick overview:
  - Previous work
  - Improvements & speedup
- Details:
  - OpenMP overhead
  - Speedup of B2 Subroutines
  - Bottlenecks
  - Correctness

# Previous work & Amdahl's law

- 4 OpenMP parallel loops by F. Reid
- Speedup is limited by the parallel fraction
- Testcase: ITER H+T+He+Be+Ne+W: 98 species



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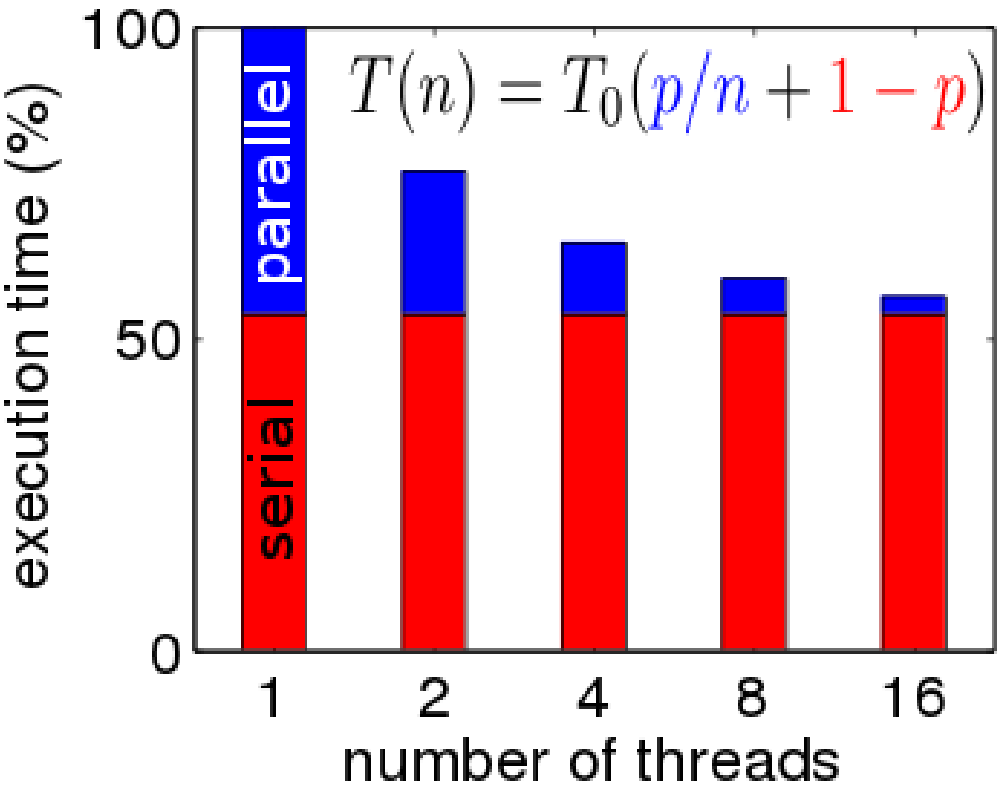
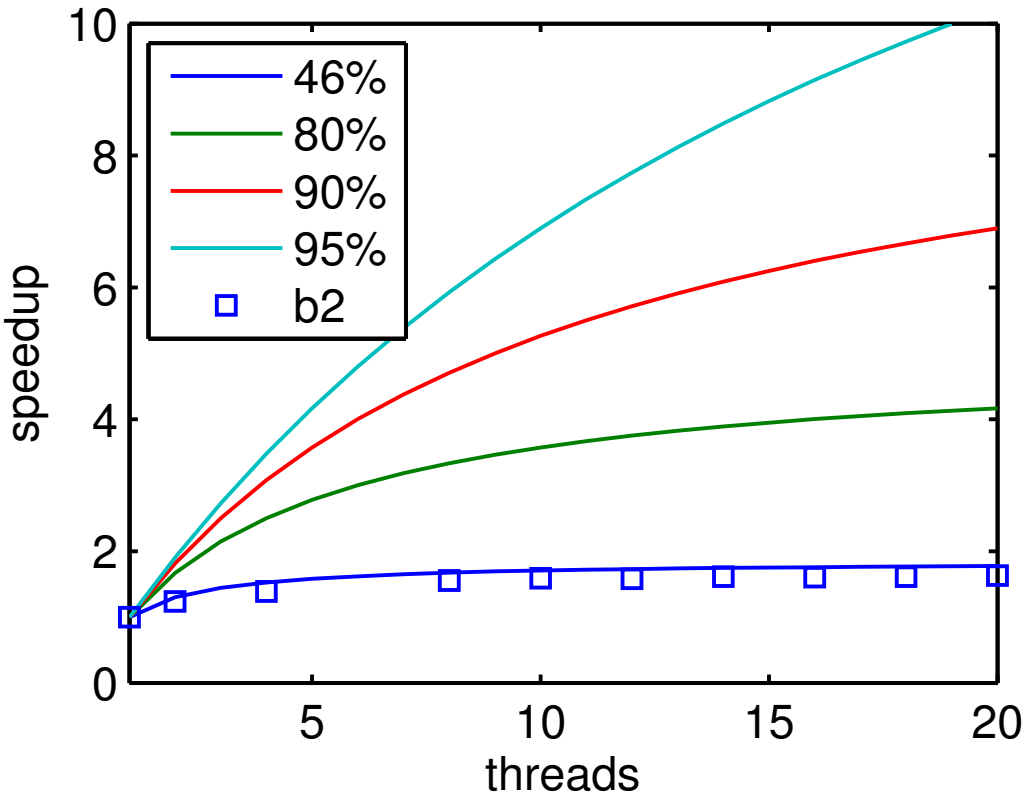
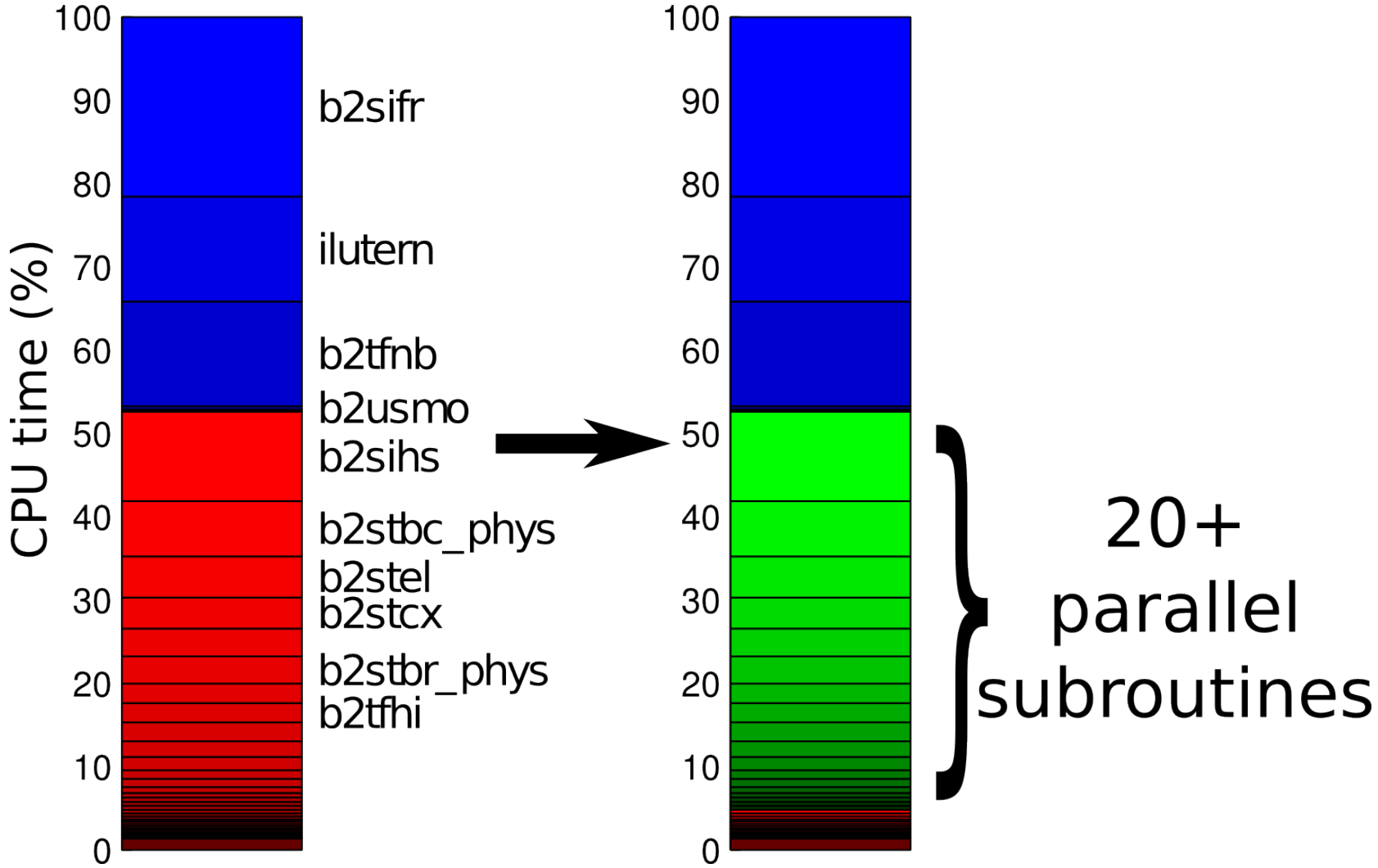


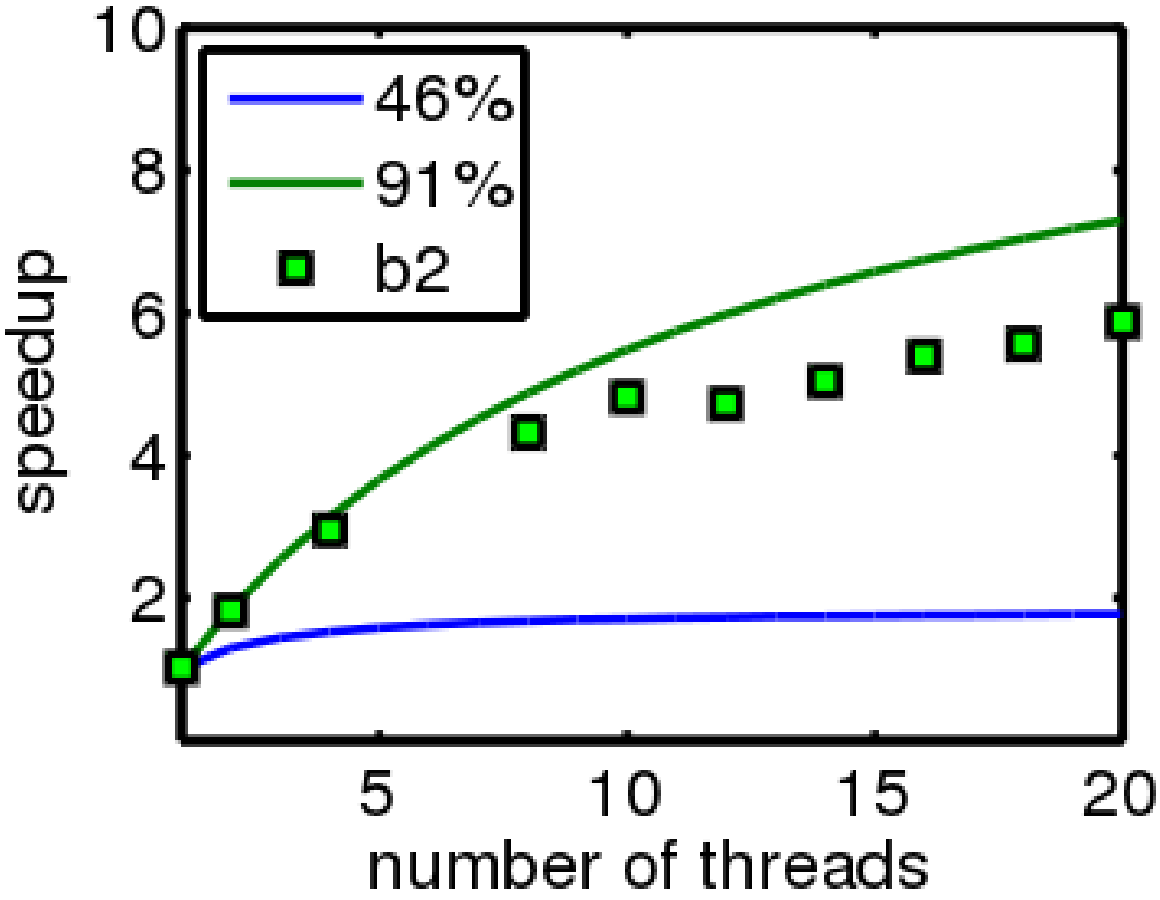



# Improving parallelization



# B2 speedup: 6x

- Testcase: ITER D+T+He+Be+Ne+W
- Parallel fraction 91%
- Run on 1 Ivy-Bridge node (2x10 cores) @ RZG Hydra



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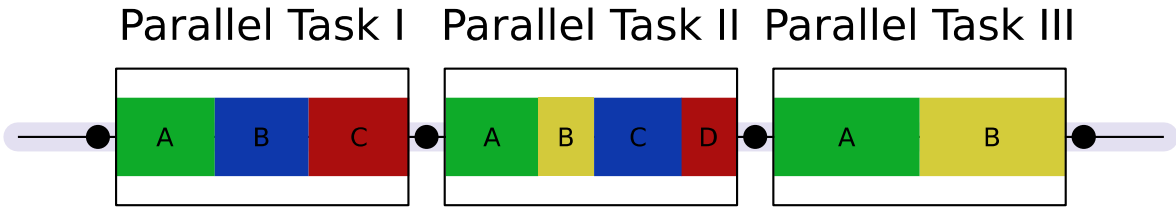
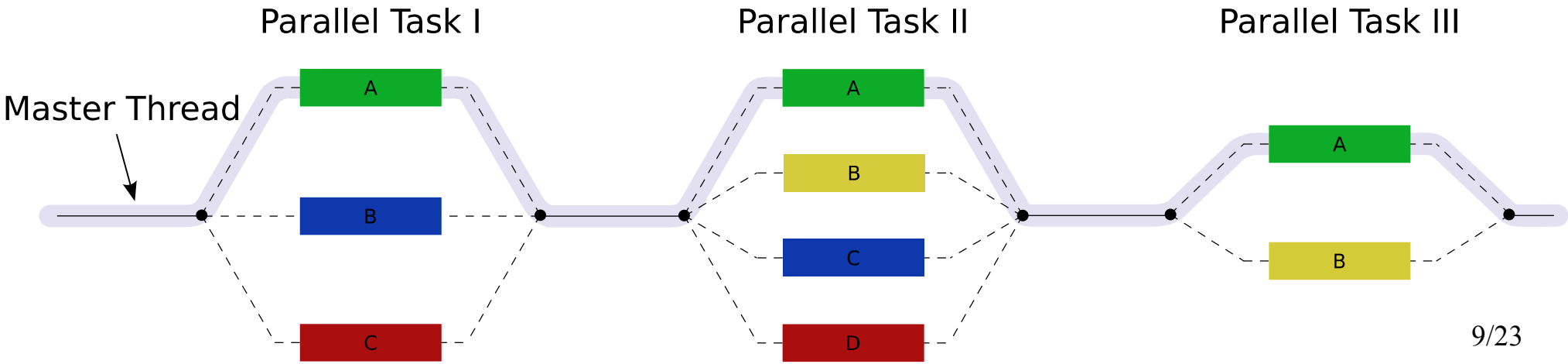
# OpenMP parallelization

Sequential execution:



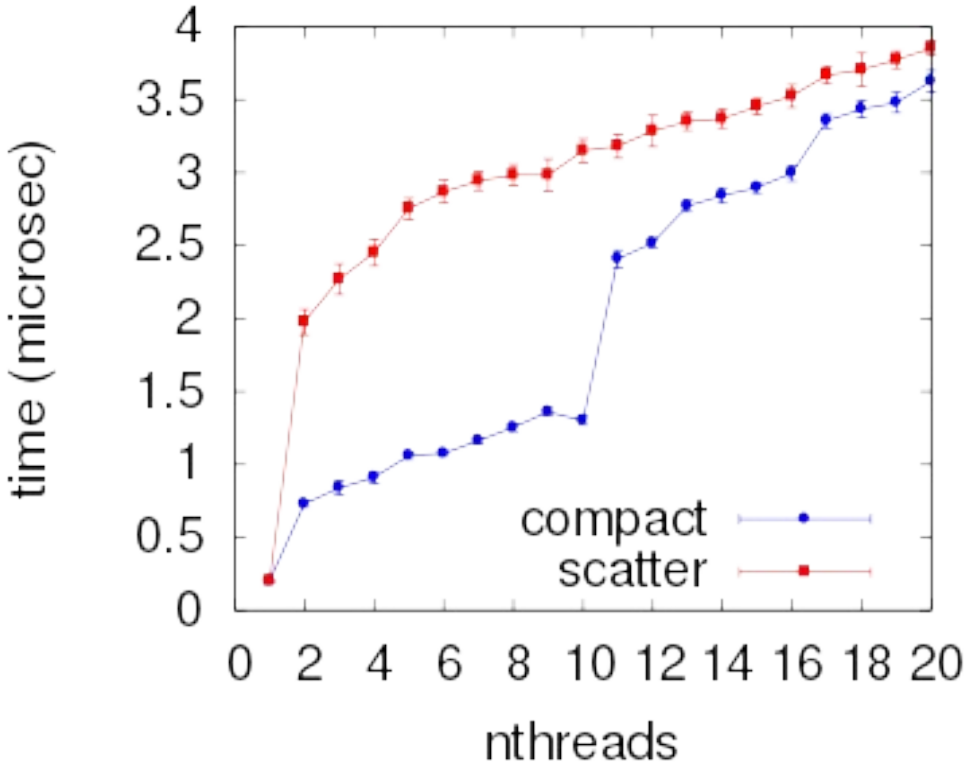

Parallel execution:



# OMP loop overhead: 1-4 µs

## EPCC OpenMP Microbenchmarks

| Sequential code |
| --- |
| **for** ( j = 0 ; j < innerreps ; j ++) {<br>  delay ( delaylength ) ;<br>} |
| Parallel code |
| **for** ( j = 0 ; j < innerreps ; j ++) {<br>#pragma omp **parallel for**<br>  **for** ( i = 0 ; i < nthreads ; i ++) {<br>  delay ( delaylength ) ;<br>  }<br>} |



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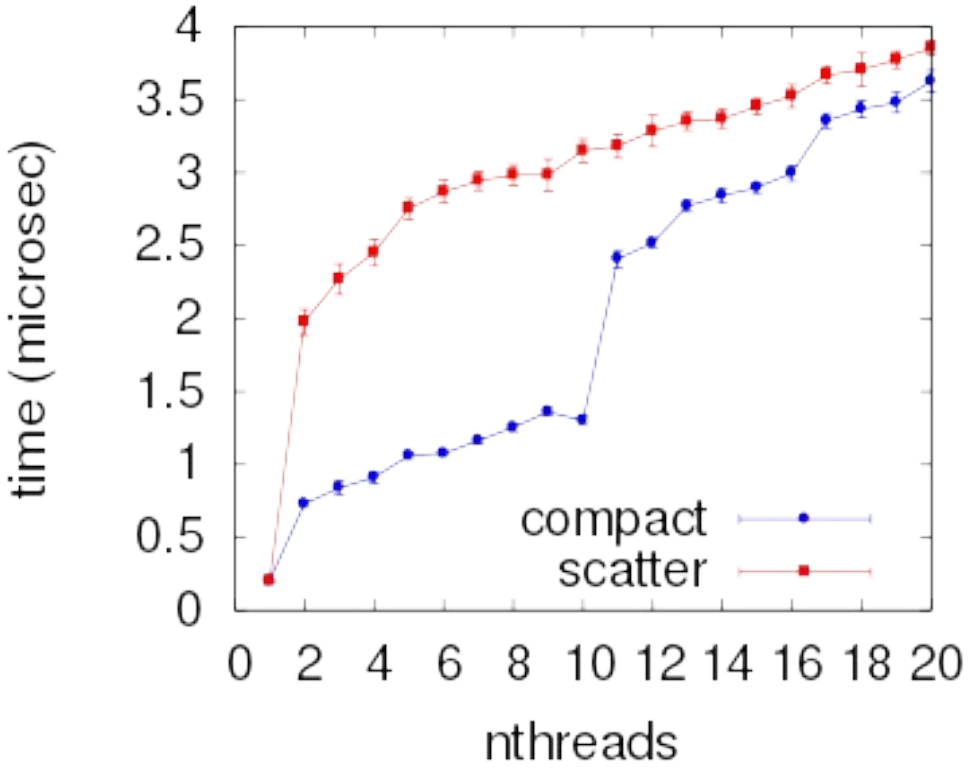
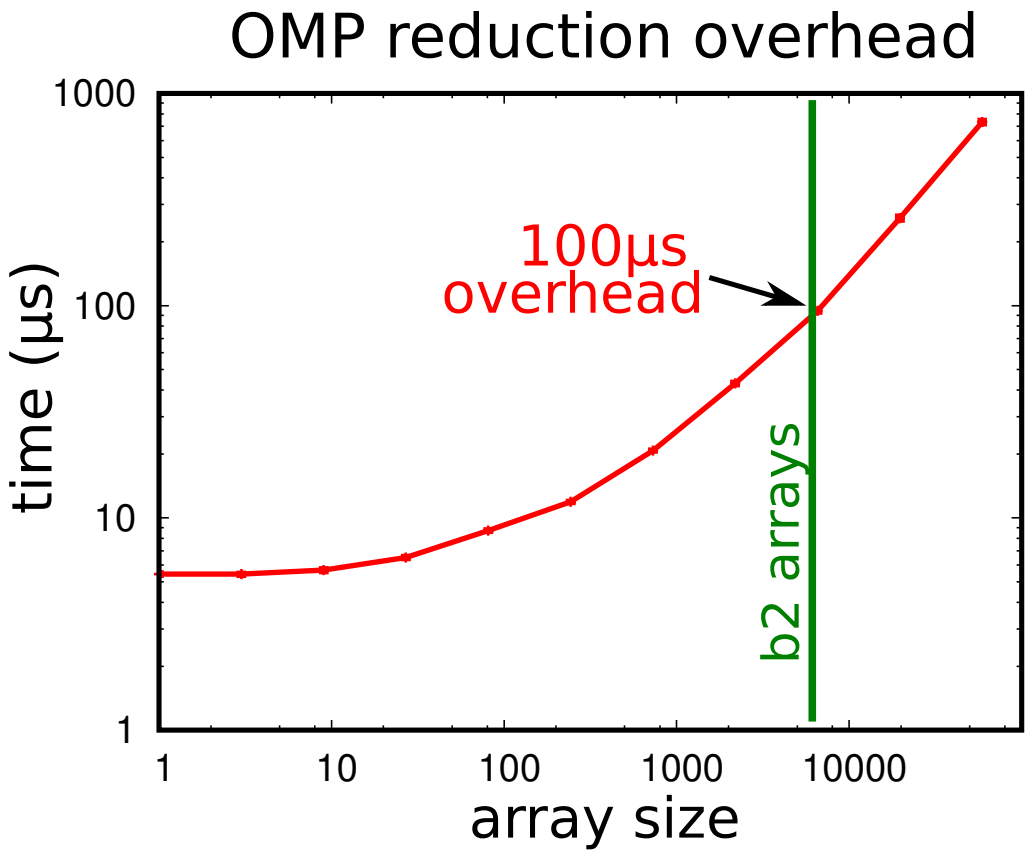



- Memory transfer of 1 radial grid (98x38 elements) 4µs
- Arithmetics with several grids >20µs
- 3D arrays (species & radial grid) (98x98x38) ~ 1ms

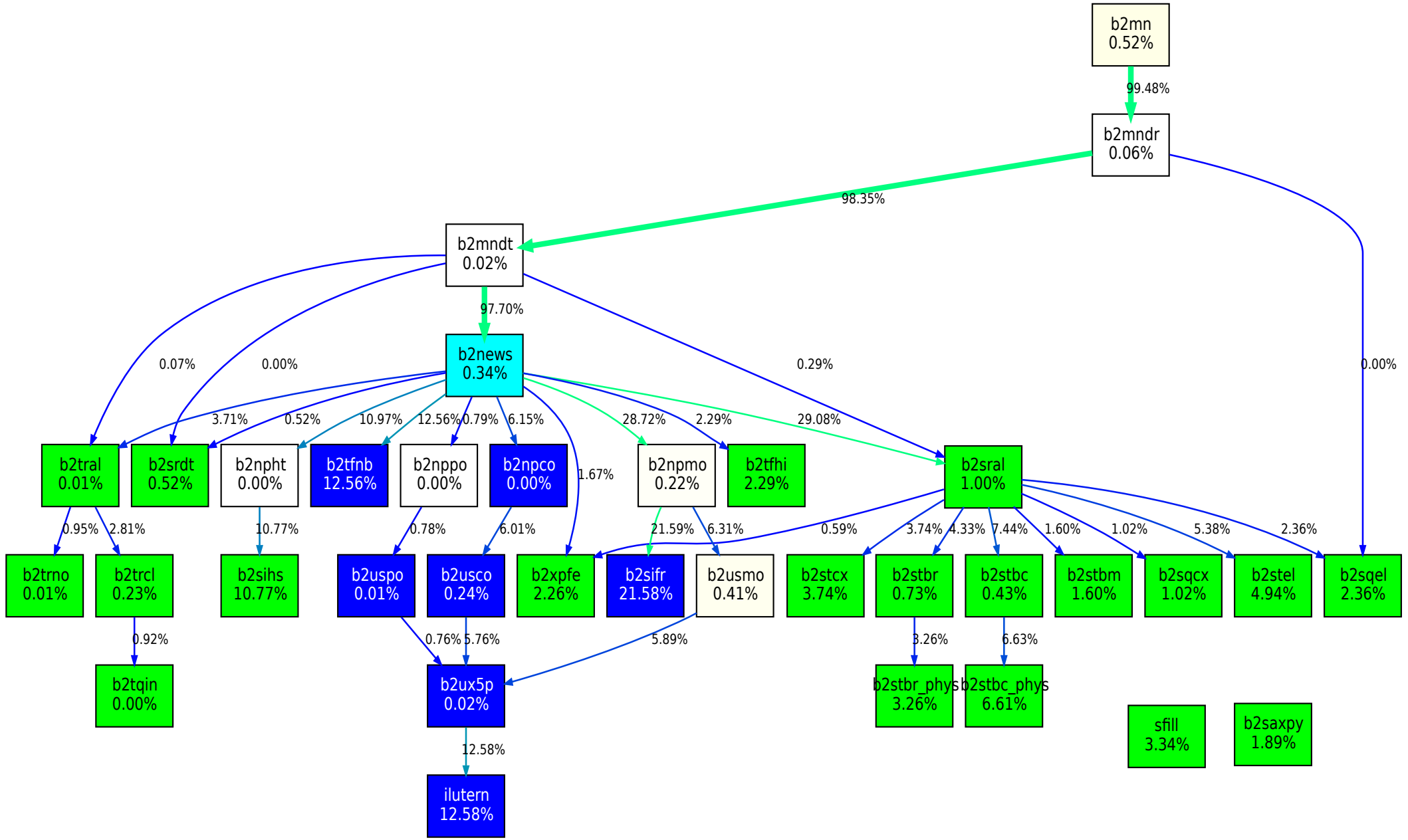
# OpenMP Reduction Overhead

```
!$omp parallel do private(is,ix,iy) &
!$omp reduction(+: s)
do is = 0, ns-1
  do iy = -1, ny
    do ix = -1, nx
      s(ix,iy) = s(ix,iy) + &
      a(ix,iy,is)* r(ix,iy,is) &
      * vol(ix,iy) * ne(ix,iy)
    enddo
  enddo
enddo
```

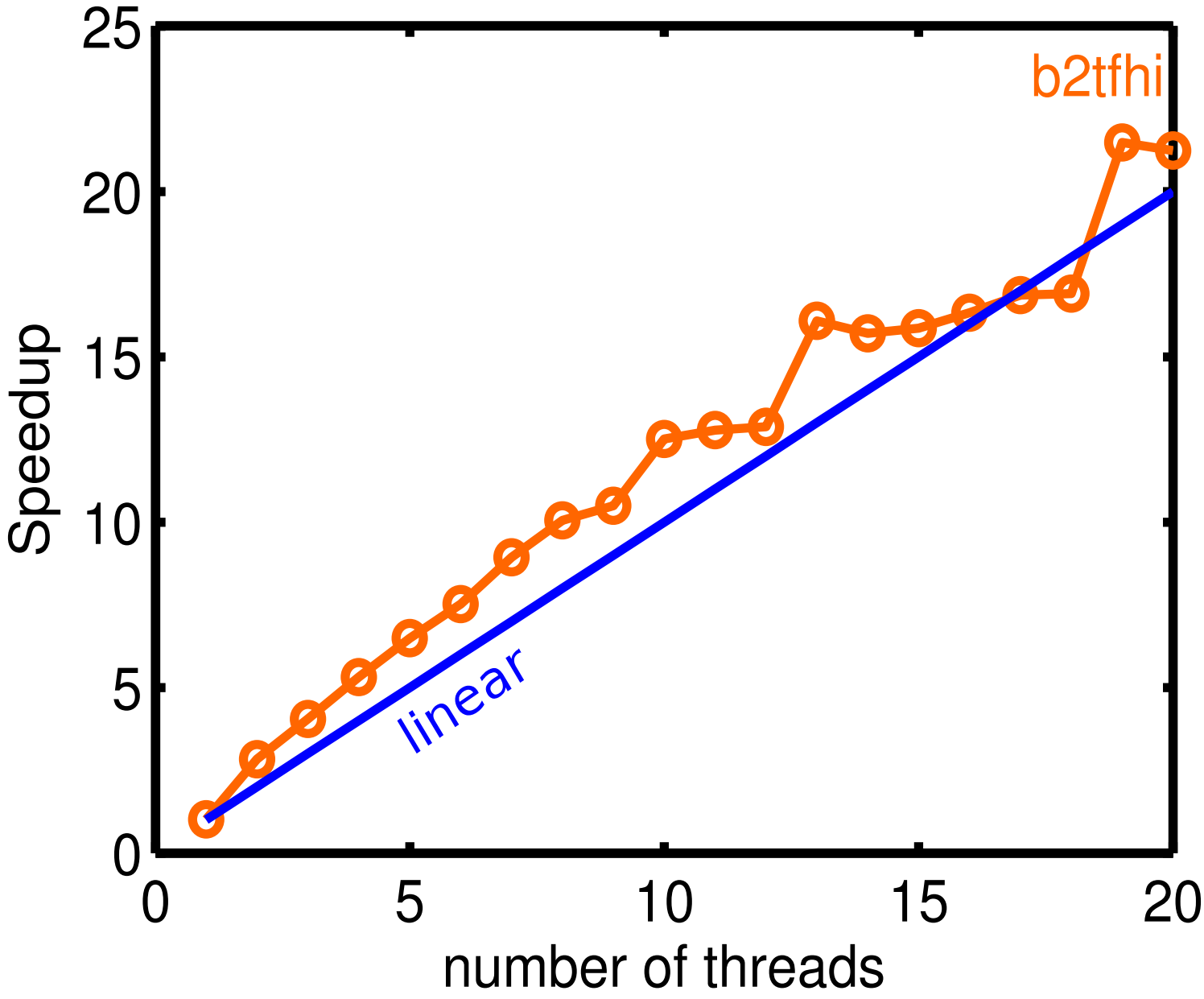



500 µs execution time

# B2 Callgraph



# Speedups


25
20
15
10
5
0
Speedup
0
5
10
15
20
number of threads
b2tfhi
linear

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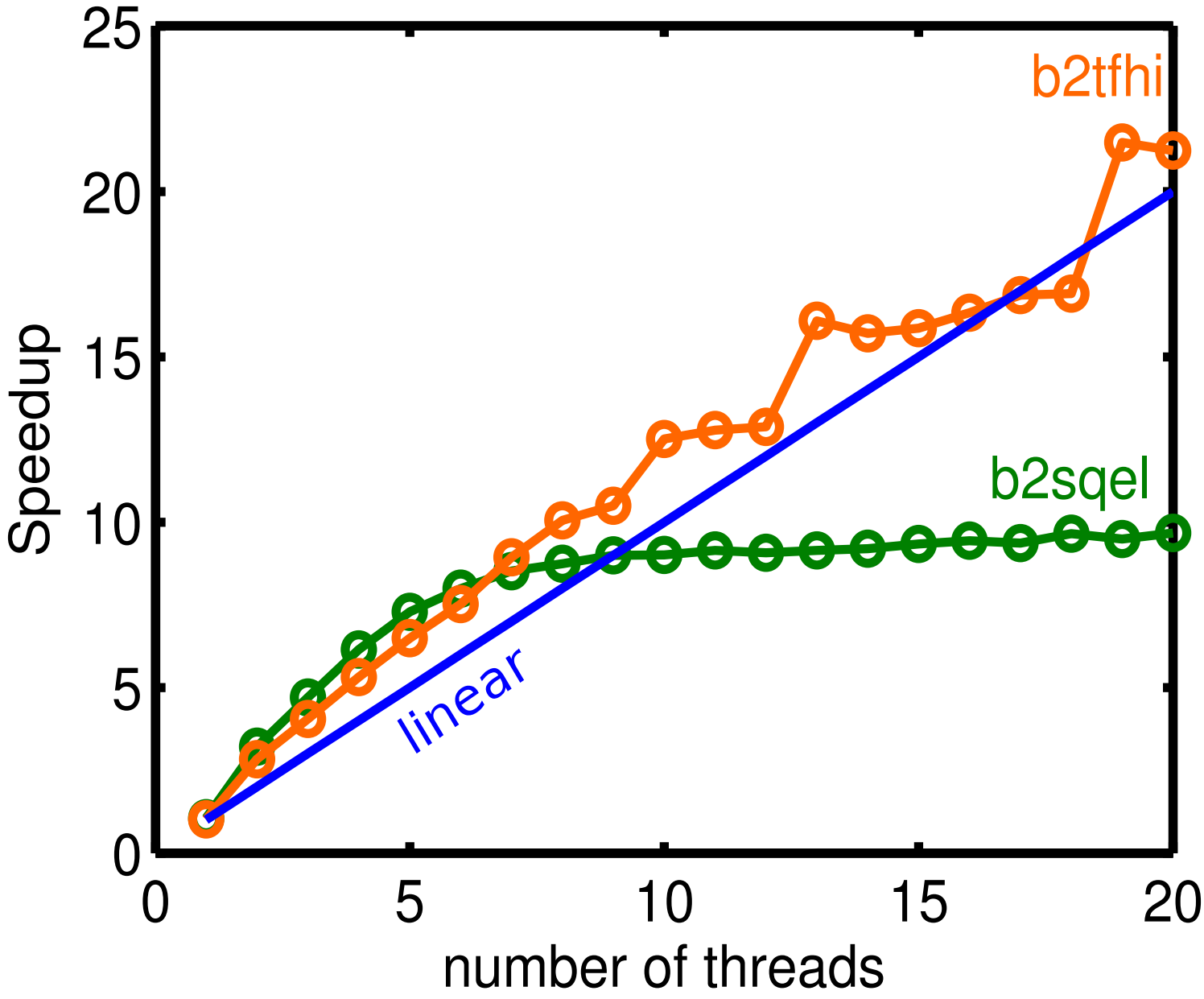
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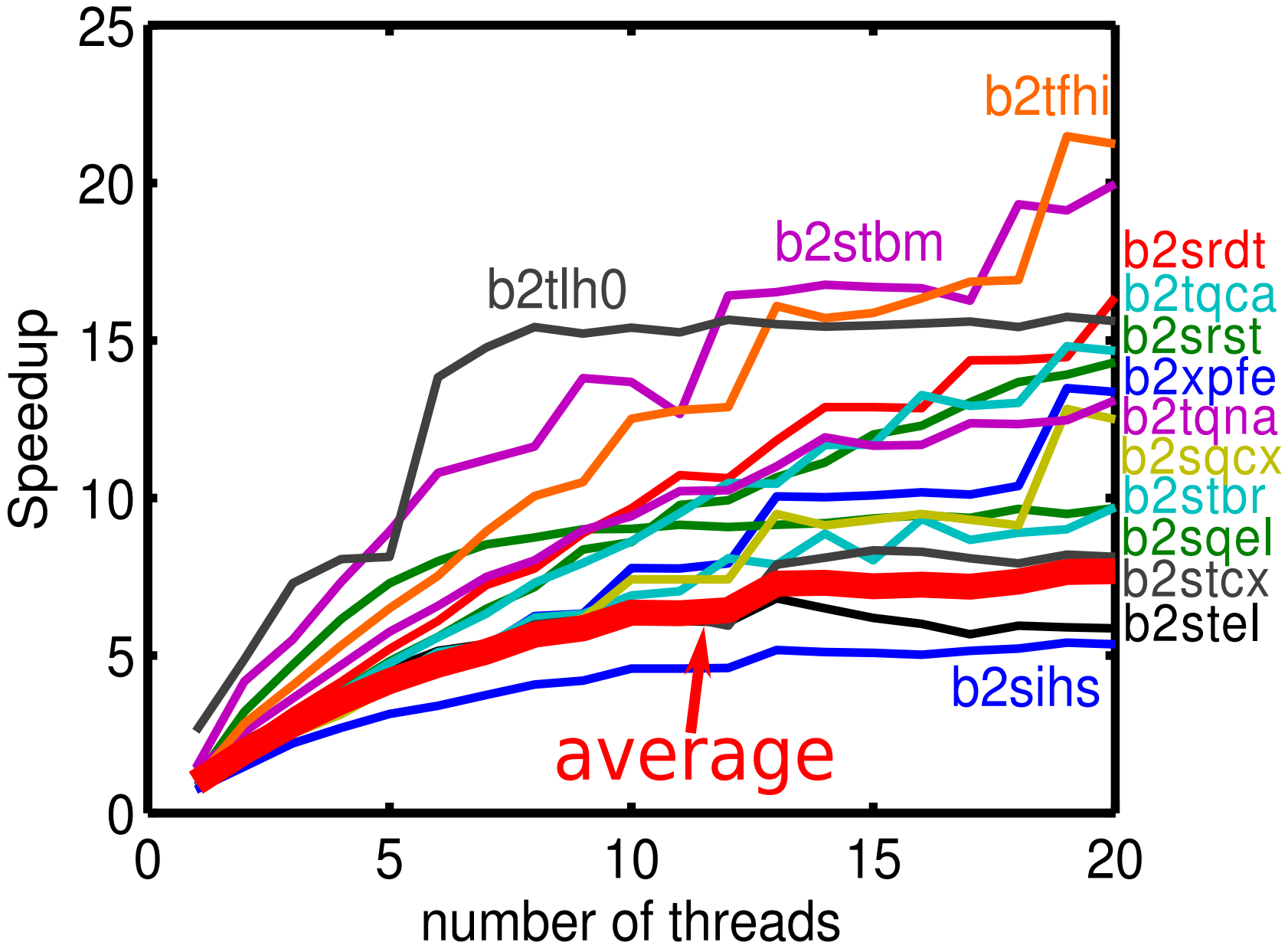
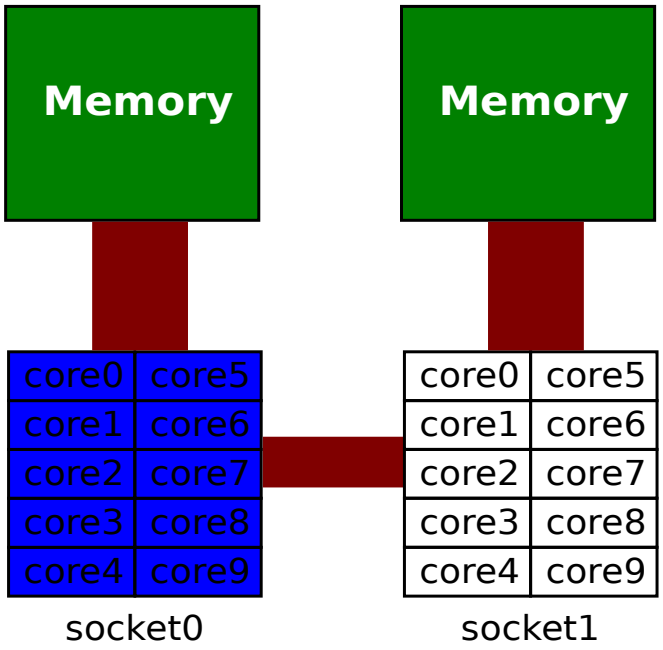
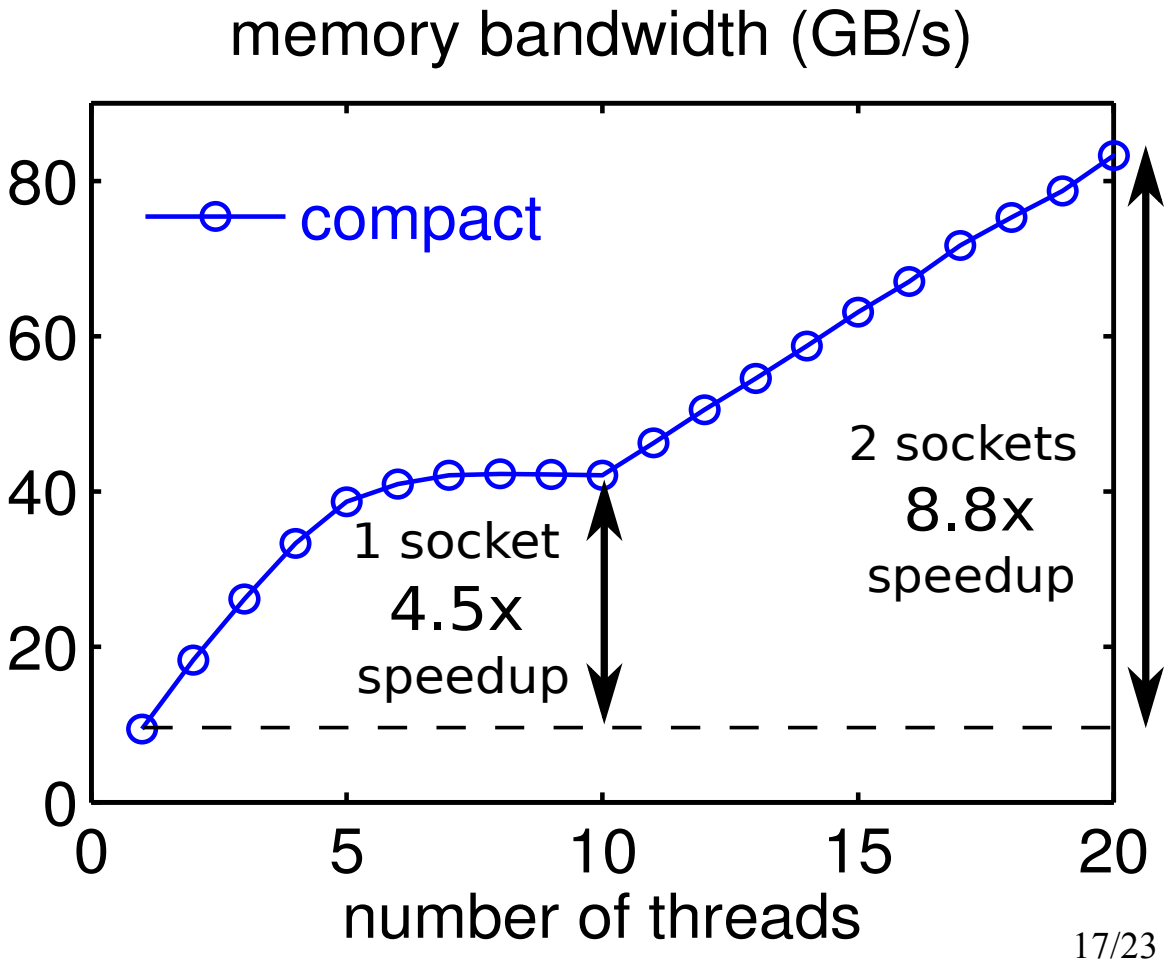

25
20
15
10
5
0
Speedup
0
5
10
15
20
number of threads
b2tfhi
b2stbm
b2tlh0
b2srdt
b2tqca
b2srst
b2xpfe
b2tqna
b2sqcx
b2stbr
b2sqel
b2stcx
b2stel
b2sihs
average

# Memory bottleneck

- Lot of data, few FLOPs / data
- Memory bandwidth limits the achievable speedup



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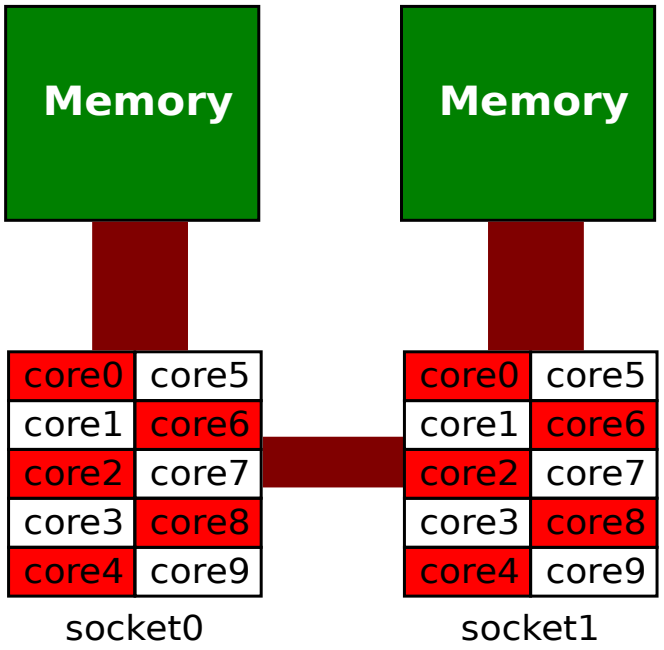
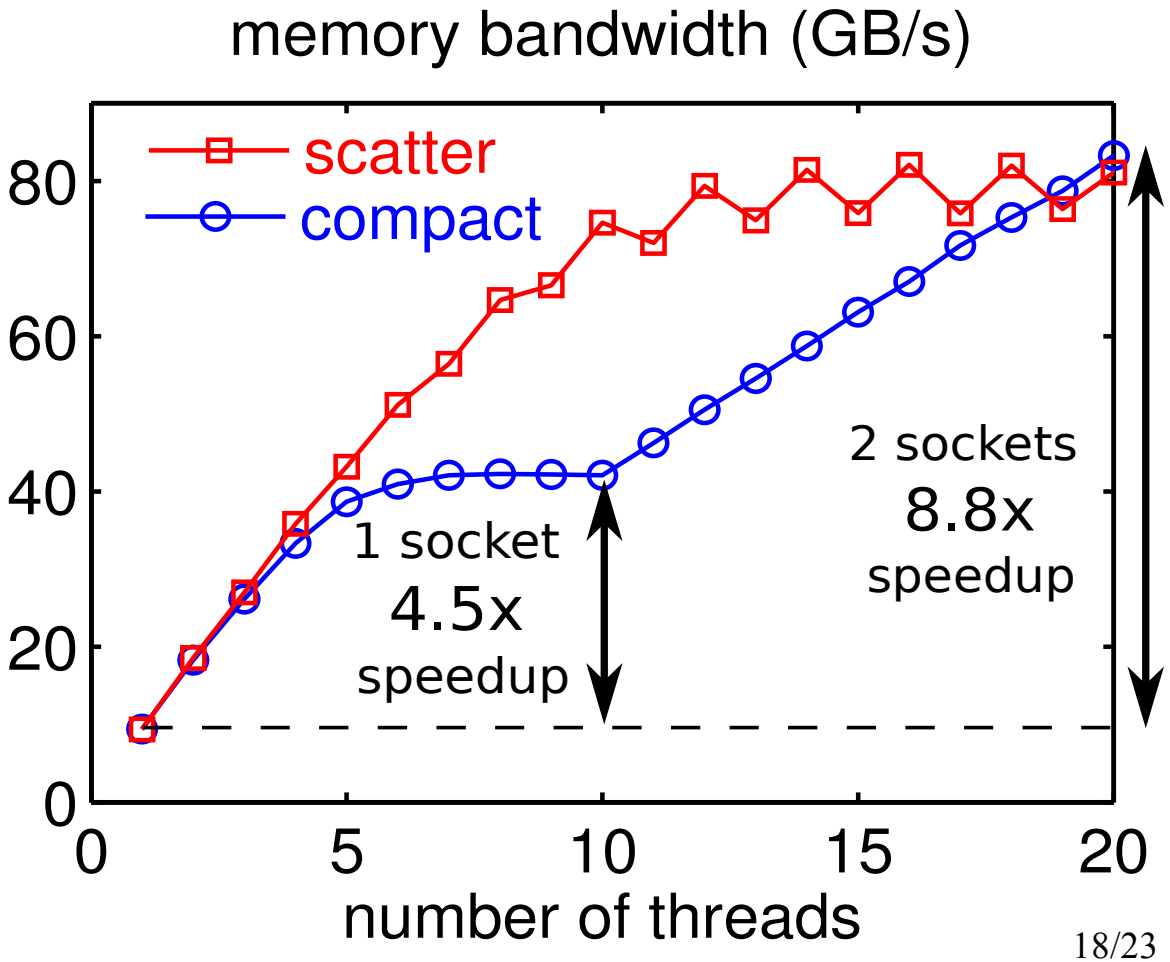
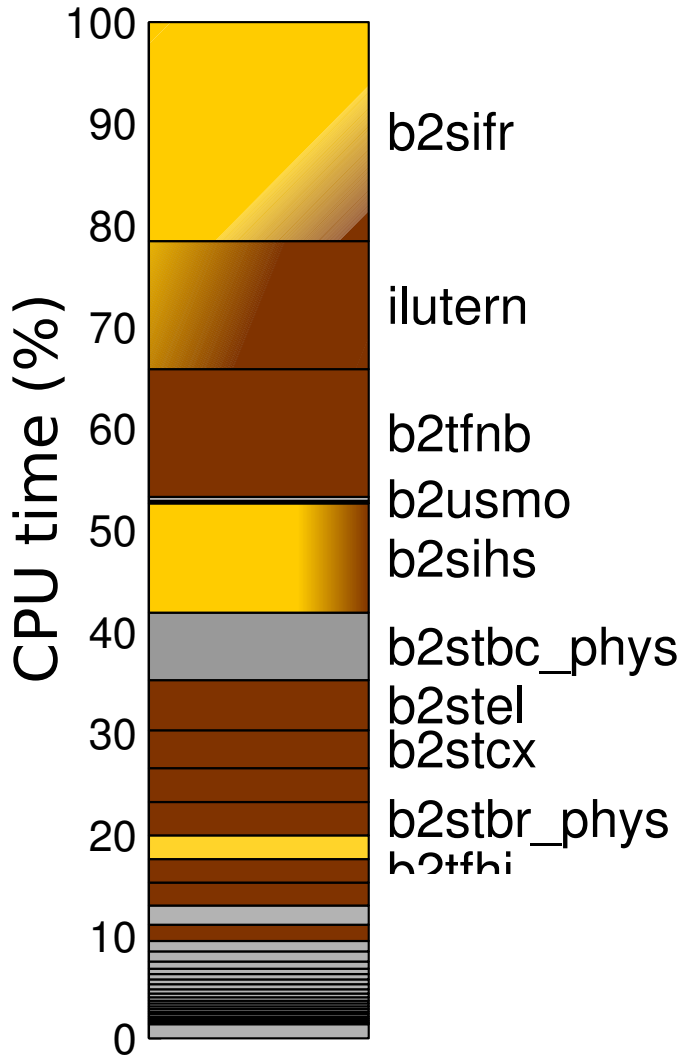
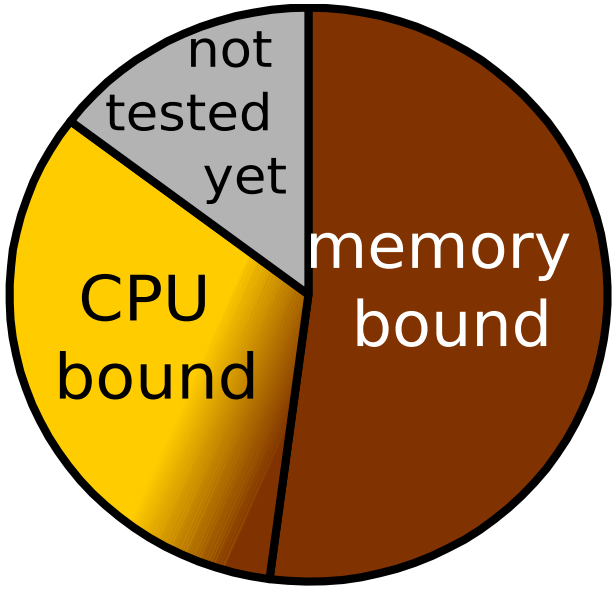


# Bottlenecks in B2



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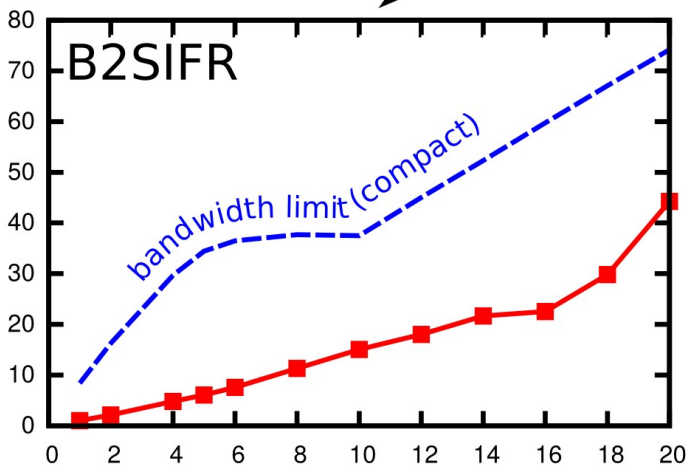
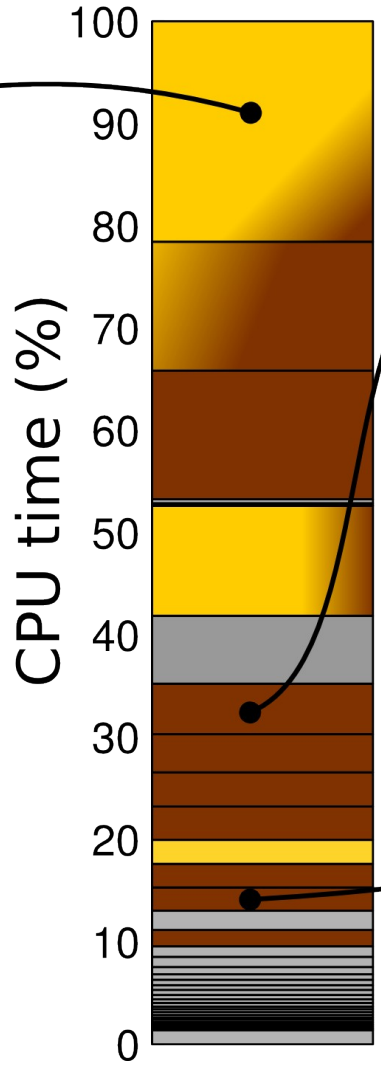
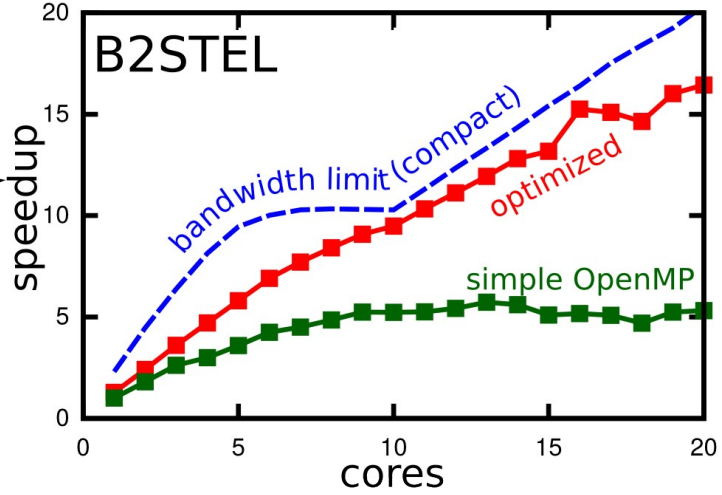
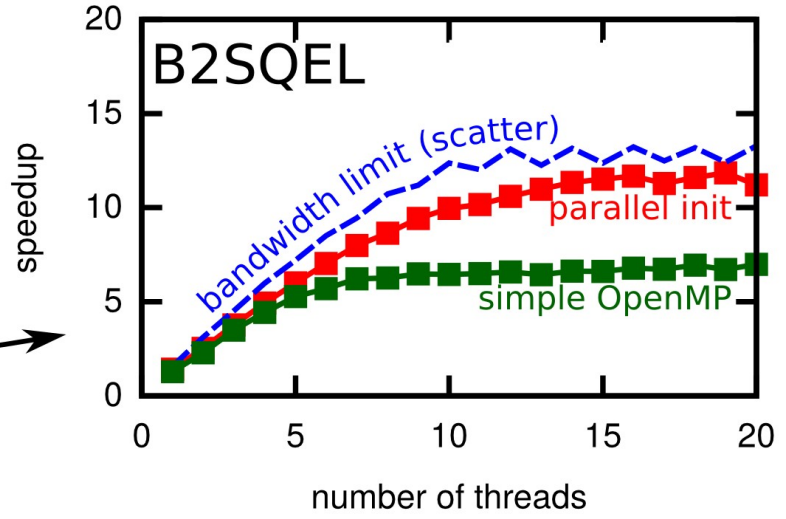


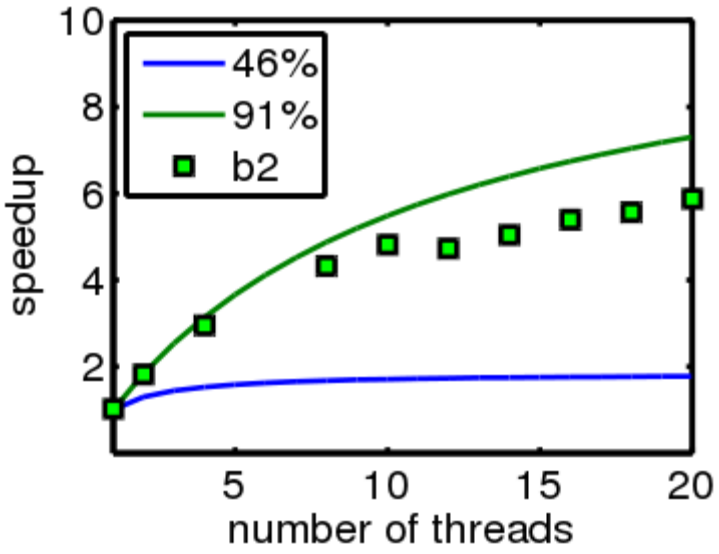
# Correctness


- Unit test framework:
  - Test programs generated
  - For every modified subroutine
  - Test all input/output data and side effects
- Complete tests:
  - Test the whole program
  - Bit-identical results

    (until a certain point in optimization)
- Tests are successful: relative error is $10^{-13}$

# Summary

- OpenMP parallelization of B2.5:
  - 20+ subroutines parallelized
  - 6x speedup for ITER test case
  - Carefully tested




- Ongoing work:
  - Couple OpenMP B2 with MPI EIRENE
  - Real world test cases
  - Further improvements to reach bottleneck

# Unit test generator


```
subroutine foo(a, b, c)
   use mydata, only :  matrix
   integer :: a
   real :: b
   real, dimension(:) :: c
   ....
end subroutine foo
```

1. Forcheck

| VARIABLE | TYPE | RANK | SIZE | OPERATIONS | LINES |
|---|---|---|---|---|---|
| A | I | | 4 | P O | 25 27 |
| B | R | | 4 | P O | 25 28 |
| C | R | 1 | | P O | 25 29 |
| MATRIX | R | 2 | | O | 26 32 |

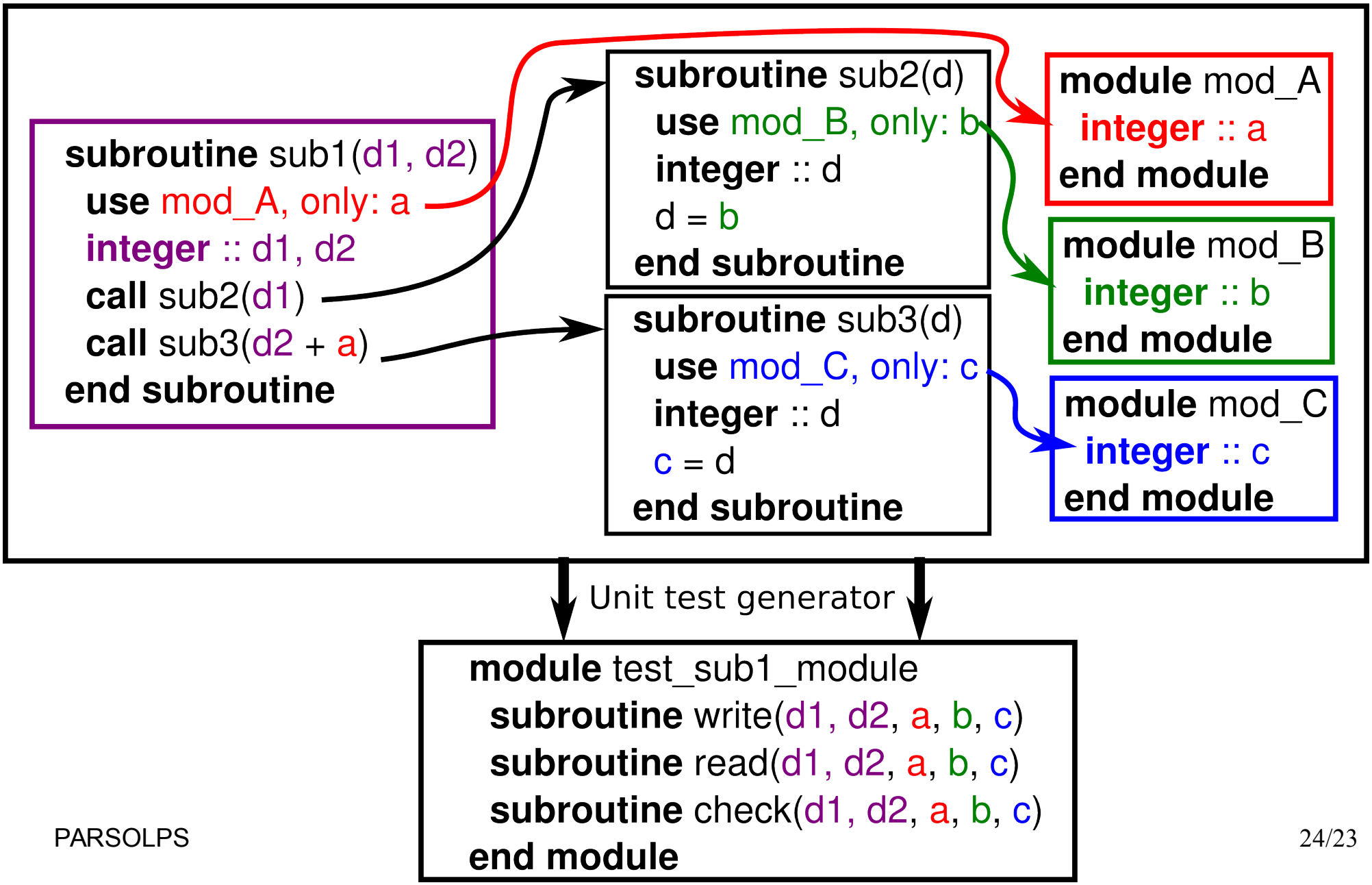
```
#ifdef UNITTEST_SAVE_DATA
      call write_foo(A, B, C, lbound(C), ubound(C),
   &  MATRIX, lbound(MATRIX), ubound(MATRIX))
#endif
```

2. python unit test generator

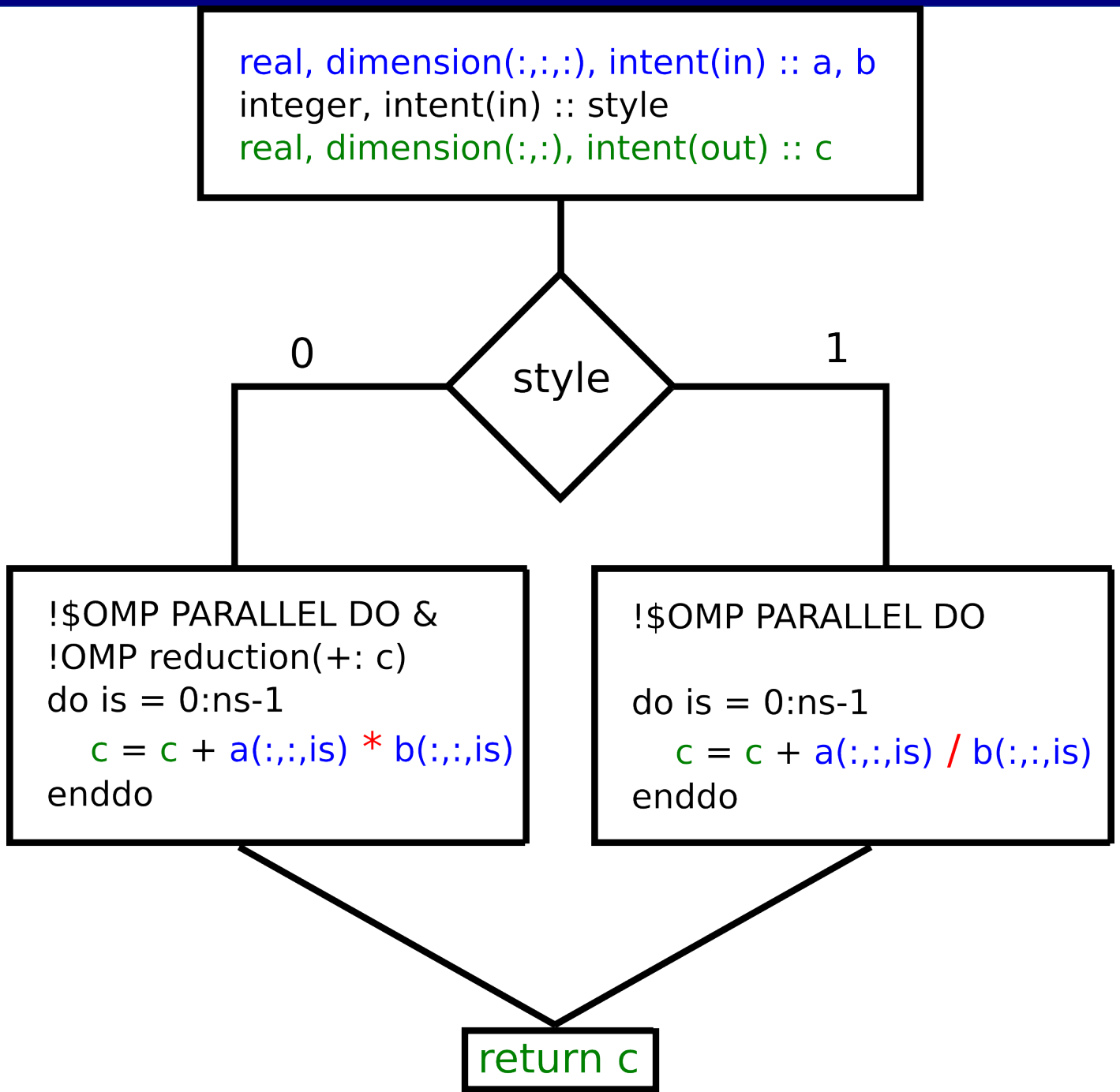
```
program test_foo
  use test_foo_module
  ...
  call setvar_foo(A, B, C, MATRIX)
  call foo(a, b, c)
  ...
  check_results(A, B, C, MATRIX)
  ....
end program
```

```
module test_foo_module
  public :: init, read_foo, write_foo
  public :: setvar_foo
  public :: store_results, check_results
...
end module
```

# Test side effects

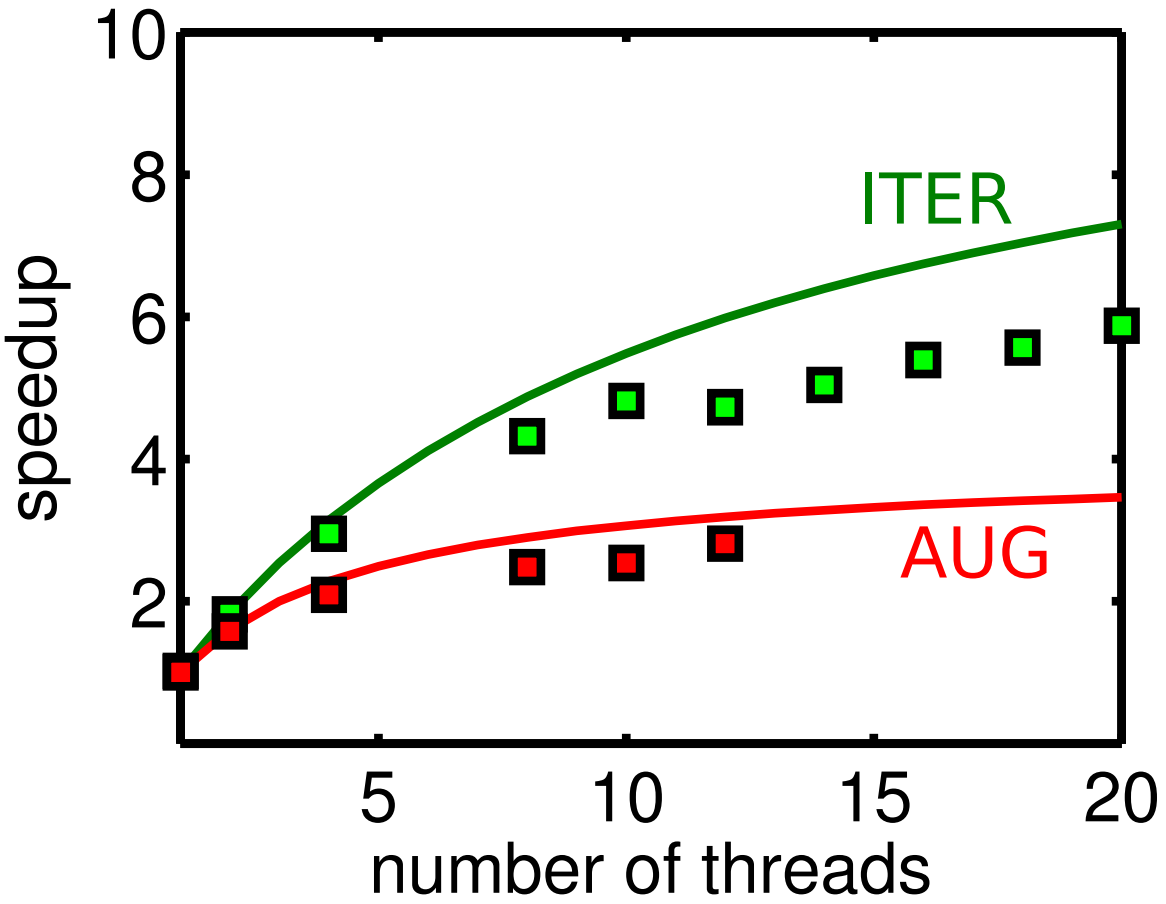


# Full coverage



# B2 Testcases

- Parallel fraction depends on simulation parameters
- Timesteps, switches, compiler options
- ITER D+T+He+Be+Ne+W,  AUG_16151_D+C+He



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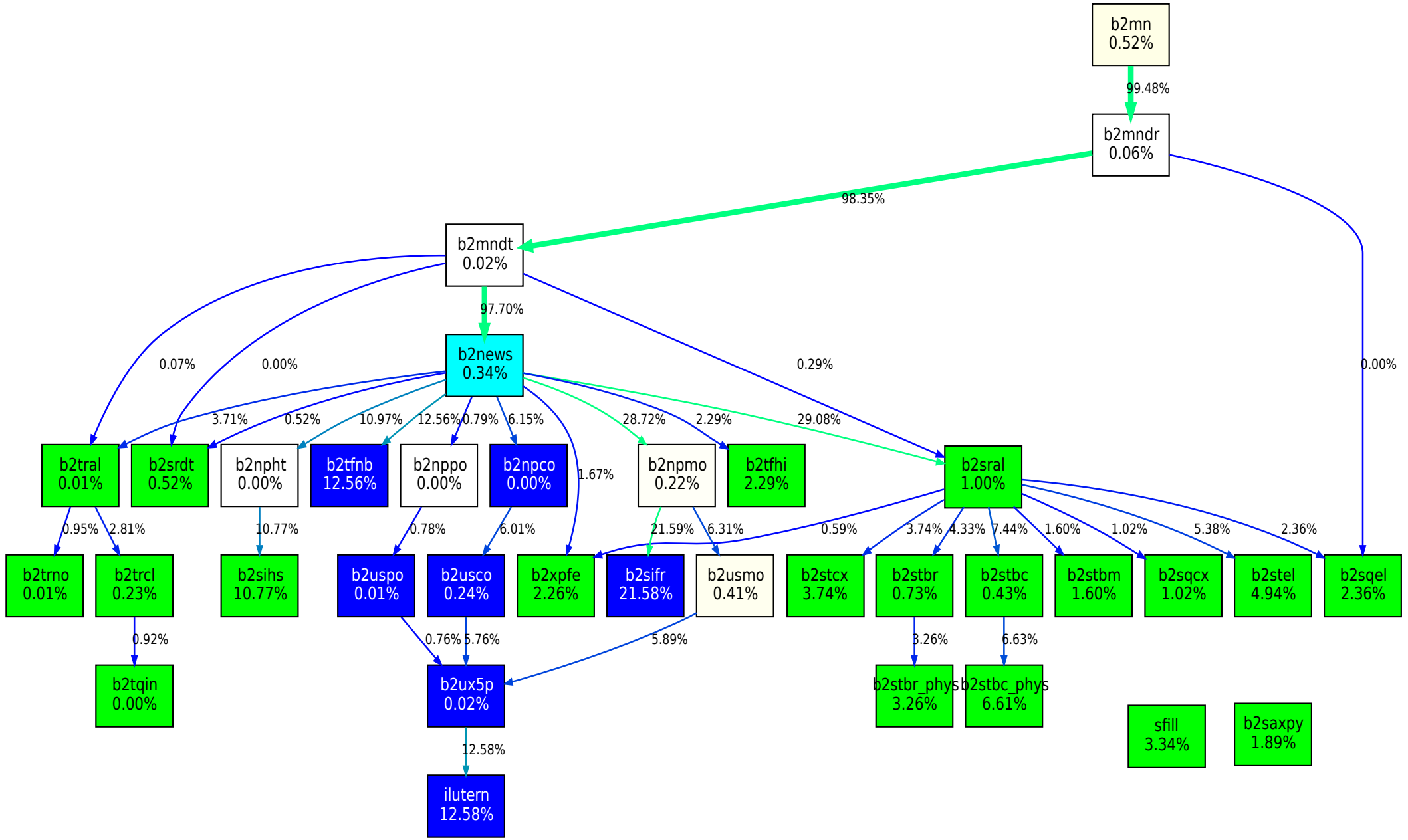

# Next year: Hybrid MPI-OpenMP